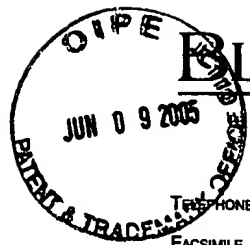


DSD



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June 7, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: **LETTER OF RECORD**
Original Letters Patent No. 6,519,754
Issued: February 11, 2003
For: METHODS AND APPARATUSES FOR DESIGNING INTEGRATED
CIRCUITS
Inventor: McElvain et al.
Our File No. 002986.P006

Dear Sir:

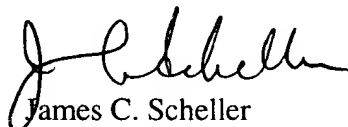
Please make record in the file of the above-identified Letters Patent the following typographical errors:

In Claim #22, Line #5, please delete "first.RTL" and insert - - first RTL - -.

In Claim #51, Line #1, please delete "herein" and insert - - wherein - -.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP


James C. Scheller
Reg. No. 31,195

JCS/nsf